

Appl. No. : 09/803,422
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AMENDMENTS TO THE SPECIFICATION

Please amend the specification as indicated below.

Please amend the paragraph beginning on page 5 at line 3 as follows:

In Figure 2, the transceiver 201 includes a system controller 210, a receiver block 211, a transmitter block 213, a transmitter-multiplexer controller 212, and a front end 214. In one embodiment, the front end 214 is an analog front end that provides signal access to and from the communications medium. Data to be transmitted by the transceiver 201 is provided to an input data port 218, and data received by the transceiver 201 is provided at a output data port 219. One skilled in the art will recognize that the input data port ~~219~~ 218 and the output data port ~~218~~ 219 can be combined into a bi-directional data port. A data output of the front end 214 is provided to a data input of the receiver block 211. A data output of the receiver block 211 is provided to a received-data input of the system controller 210. A transmit-data output of the system controller 210 is provided to a transmit-data input of the transmitter block 213. A multiplexer control output from the system controller 210 is provided to the transmitter multiplexer controller 212, and a multiplexer-address output from the multiplexer controller 212 is provided to a multiplexer-address input of the transmitter block 212.

Please amend the paragraph beginning on page 5 at line 17 as follows:

The transceiver 202 is similar to the transceiver 201, and includes a system controller ~~220~~ 226, a receiver block 221, a transmitter block 223, a transmitter-multiplexer controller 222, and a front end 224. Organization and data flow in the transceiver 202 is similar to the transceiver 201, wherein the system controller ~~220~~ 226, the receiver block 221, the transmitter block 223, the transmitter-multiplexer controller 222, and the front end 224 correspond to the system controller 210, the receiver block 211, the transmitter block 213, the transmitter-multiplexer controller 212, and the front end 214, respectively.

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Please amend the paragraph beginning on page 6 at line 10 as follows:

As shown in Figure 2, each network device typically includes a system controller, a receiver block and a transmitter block. The system controllers 210, ~~221~~ 226 coordinate the process of transmitting and receiving data between devices. Consider, for example, the transmission of a packet from the transceiver 201 to the transceiver 202. The packet is provided to the system controller ~~218~~ 210 where it is broken up into one or more fragments. The system controller 210 provides the first M fragments to the transmitter block 213 which then sends the M fragments on M channels (one fragment per channel) to the receiver block 223.